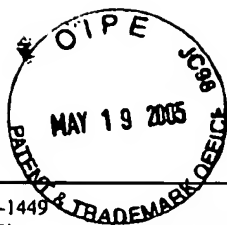


Sheet 1 of 1

Form PTO-1449 (REV. 8-83)		US Department of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 104822.01		APPLICATION NO. 10/724,885	
INFORMATION DISCLOSURE STATEMENT  (Use several sheets if necessary)				APPLICANT Makoto KUDO et al.			
				FILING DATE December 2, 2003		GROUP 2184	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	
BMB	1	JP A 01-287752 (w/Abstract)	11/20/1989	Japan			
BMB	2	JP A 02-264339 (w/Abstract)	10/29/1990	Japan			
BMB	3	JP A 06-103104 (w/Abstract and English-language translation)	04/15/1994	Japan			
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
	4	Isao Yoshida, "Function and Application of Serial Port", Transistor Gijutsu, Vol. 32, No. 10, October 1, 1995, pp. 204-247 (w/partial English language translation of article)					
EXAMINER  Bryce D. Bong				DATE CONSIDERED 11/13/05			
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

Date: May 2, 2005

Sheet 1 of 1

Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 104822.01		APPLICATION NO. 10/724,885	
INFORMATION DISCLOSURE STATEMENT  (Use several sheets if necessary)				APPLICANT Makoto KUDO et al.			
				FILING DATE December 2, 2003		GROUP 2184	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	
BBB	1	JP A 01-287752 (w/Abstract)	11/20/1989	Japan			
BBB	2	JP A 02-264339 (w/Abstract)	10/29/1990	Japan			
	3	JP A 06-103104 (w/Abstract and English-language translation)	04/15/1994	Japan			
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
BBB	4	Isao Yoshida, "Function and Application of Serial Port", Transistor Gijutsu, Vol. 32, No. 10, October 1, 1995, pp. 240-247 (w/partial English language translation of article)					
EXAMINER: <i>Byrne P. Byrne</i>				DATE CONSIDERED <i>11/15/05</i>			
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

Date: May 19, 2005

Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 104822.01		APPLICATION No.: Rule 1.53(b) Divisional of Appln. No. 09/424,667, filed 03-15-2000	
INFORMATION DISCLOSURE STATEMENT  (Use several sheets if necessary)				APPLICANTS Makoto KUDO et al.			
				FILING DATE December 2, 2003		GROUP	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	
BPN		6,314,530	11/2001	MANN			
		6,154,856	11/2000	MADDURI ET AL.			
		6,185,522	2/2001	BAKKER			
		5,771,240	6/1998	TOBIN ET AL.			
		6,484,273	11/2002	CHANG			
		6,502,210	12/2002	EDWARDS			
		6,145,123	11/2000	TORREY ET AL.			
		6,279,123	8/2001	MULROONEY			
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	
BPN	1	60-72034	04/1985	Japan			
	2	8-221297	08/1996	Japan			
	3	1-286030	11/1989	Japan			
	4	64-3745	01/1989	Japan			
	5	63-303437	12/1988	Japan			
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
		IEEE Standard Test ACCESS Port and Boundary Scan Architecture, IEEE, 1993 IEEE Std 1149.1-1990					
EXAMINER				DATE CONSIDERED			
Bryce P. Bonzo				11/15/05			
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							